MP2000 Memory Architecture

Battery Backed (SRAM)*

M Registers
S Registers
Alarm History
Data Trace Memory

FLASH Stored

Program Memory
 D Registers**
 # Registers
Definition Files
 Application Information Setting
 Module Configuration***
 Scan Time Setting
 System Configuration
 Group Definition
Ladder Programs
 Function Programs
 H (High Speed) Scan Programs
 A (At Power up) Scan Programs
 I (Interrupt) Programs
 L (Low Speed) Scan Programs
Motion Programs
Table Data**
All Other Data
 C Registers

Notes:

* Turning on the INIT switch of the CPU and applying power will clear the SRAM.
** D Registers are normally stored in FLASH. In the System Configuration definition they can be globally configured to be cleared on power-up .
*** This includes SVB Setup Parameter tab (OW8000-OWFFFF registers) values are stored in flash. All of the other I and O register values are not stored.
**** Table Data is normally stored in FLASH, but can individually be configured to battery backed during creation of table when using MP-2200 CPU-02 module only.

A general note regarding the MP2200 CPU-02, the project can be stored/loaded to/from compact flash memory.