

Motion Template Register Allocation.

M Register

Comprehensive Map

MW00000 - MW00099	System Reserve : Avoid conflict to MP930 System Reservation.
MW00100 - MW00999	Bit operation : MB00xxxx
MW01000 - MW07999 (ML01000 – ML7998)	Integer Operation (single and double length) : MW0xxxx, ML0xxxx
MF08000 - MF08999	Floating Operation : MF0xxxx
MW09000 – MW09999	CPU Internal Constants (B,W,L,F).
MW10000 - MW19999	HMI communication (Memobus definition OFFSET=10000)
MW20000 - MW32767	Optional Extend HMI, CAM Table, Table Data, etc.

Bit Map

MB001000 – MB00199F	Machine total operation (Mode, Ready, etc.). General usage (Auxiliary control, Lamps, Solenoid Valves,, etc.)
MB002000 – MB00201F	#1 Axis Operation
MB002020 – MB00203F	#2 Axis Operation
MB002040 – MB00205F	#3 Axis Operation
MB002060 – MB00207F	#4 Axis Operation
MB002080 – MB00209F	#5 Axis Operation
MB002100 – MB00211F	#6 Axis Operation
MB002120 – MB00213F	#7 Axis Operation
MB002140 – MB00215F	#8 Axis Operation
MB00???? – MB00999F	Optional after Axis Operation

Detail Bit allocation

Every 32 bits (2words) for each axis.

MB002000	Axis Normal
MB002001	Servo ON Reference
MB002002	
MB002003	
MB002004	
MB002005	Jog Forward Interlock
MB002006	Jog Reverse Interlock
MB002007	Jog Forward Reference
MB002008	Jog Reverse Reference
MB002009	Jogging
MB00200A	Homing in progress
MB00200B	Home Completed detected
MB00200C	Home completed Latch
MB00200D	
MB00200E	
MB00200F	

Continue

Detail Bit allocation (continue)

Every 32 bits (2words) for each axis.

MB002010	Product Reference
MB002011	Producing
MB002012	
MB002013	
MB002014	
MB002015	
MB002016	
MB002017	
MB002018	
MB002019	
MB00201A	
MB00201B	
MB00201C	
MB00201D	
MB00201E	Running
MB00201F	Zero Speed

CPU Internal constants for each axis

Every 20 words for each axis starting from MW09010.

MW09010	Gear Ratio [motor revolution] [N]
MW09011	Gear Ratio [machine axis revolution] [N]
MW09012	Motor Rated Speed [rpm]
MW09013	
MW09014	
MW09015	
MW09016	
MW09017	
MW09018	Jog Speed [unit varies with SVA or SVB]
MW09019	Homing Approach Speed [unit varies with SVA or SVB]
MW09020	Homing Creep Speed (SVA)[0.01%N]
MW09021	
MW09022	
MW09023	
MW09024	
MW09025	
MW09026	
MW09027	
MW09028	
MW09029	

D Register

DW00000 – DW00008	Bit Operation for General Purpose.
DW00009	Bit Operation for One Shot Pulse
DW00010 – DW00019	Word Operation : General and/or Timer
DW00020 – DW00025	Optional Word, Long word and Floating
DW00026	Word value accumulator
DW00027	Logic value accumulator
DL00028	Long value accumulator
DF00030	Float value accumulator
DW31 - DW????	Optional Word, Long word and Floating

Note : Expand D Register range up to 16k words as necessary.
Expanding D register range consumes Program Memory.
Too much Expansion can cause Programing Memory shortage.

Register

Optional.
Use for Drawing Internal Constants
Too much # Register can cause Programing Memory shortage.

C Register

Optional.
Use for Table Data, CAM Table, etc.

Undefined Register

Bit

Undefined Input Bit	MB00199A
Undefined Output Bit	MB00199B
Undefined General Bit	MB00199F

Word

Undefined Input Word	MW07997
Undefined Output Word	MW07998
Undefined General Word	MW07999